




CUSTOMER NO. 32,986  
IP STRATEGY GROUP, P.C.  
Intellectual Property Law Office  
P.O. Box 700640  
San Jose, CA 95170-0640

PATENT APPLICATION  
ATTORNEY DOCKET NO. ANTAP004/IC0105

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

<p>In re application of: ACHLER Serial No.: 10/026,679 Filed: December 17, 2001</p> <p>Title: INTERFACE CIRCUITS FOR MODULARIZED DATA OPTIMIZATION ENGINES AND METHODS THEREFOR</p>	<p>Group Art Unit: 2666 Examiner: RAO, SEEMA SRINIVAS Docket: ANTAP004/IC-0105 Confirmation No.: 7042</p> <p>CERTIFICATE OF MAILING I hereby certify that this correspondence is being deposited with the US Postal Service as First Class Mail in a postage-paid envelope addressed to the Assistant Commissioner for Patents, Washington, DC 20231 on November 29, 2004.</p> <p>Signed:  Hanh H. Bui</p>
---	--

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR §§ 1.56 AND 1.97(c)**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

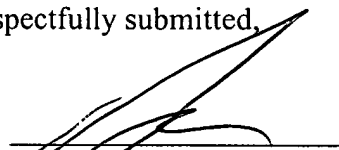
The references listed in the attached PTO Form 1449, copies of which are attached, may be material to the patentability of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§ 1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the patentability of this application does not exist, or that these references indeed constitute prior art.

If the Commissioner determines that additional fees are required to process this Information Disclosure Statement, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-2284 (Order No. ANTAP004).

Respectfully submitted,

By:

  
Joseph Nguyen  
Reg. No. 37,899

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: ACHLER Serial No.: 10/026,679 Filed: December 17, 2001  Title: INTERFACE CIRCUITS FOR MODULARIZED DATA OPTIMIZATION ENGINES AND METHODS THEREFOR	Group Art Unit: 2666 Examiner: RAO, SEEMA SRINIVAS Docket: ANTAP004/IC-0105 Confirmation No.: 7042
--	---

**INFORMATION DISCLOSURE STATEMENT**

**US PATENT DOCUMENTS**

Examiner Initials	Cite No.	Document Number	Publication Date	Name of Patentee or Applicant	Reference to Related Case

**FOREIGN PATENT DOCUMENTS**

Examiner Initials	Cite No.	Document Number	Publication Date	Applicant	Reference to Related Case	T

**OTHER DOCUMENTS**

Examiner Initials	Cite No.		T
		Robert W. Kembel; Fibre Channel: A Comprehensive Introduction", Northwest Learning Associates, Inc., Tucson, AZ 2000	
		Mark Nelson; "LZW Data Compression", <a href="http://www.dogma.net/markn/articles/lzw/lzw.htm">http://www.dogma.net/markn/articles/lzw/lzw.htm</a> . Originally published in October 1989 Dr. Dobb's Journal (2800 Campus Drive, San Mateo, CA. www.ddj.com)	
		Terry A. Welch; "A Technique for High Performance Data Compression", IEEE Computer 17(6), June 1984, pp. 8-19	

Examiner Signature		Date Considered	
-----------------------	--	--------------------	--